

**ENGINEERING CHANGE NOTE:**

ECN #:

396

**ASSEMBLY:** BMW4011E**DESCRIPTION:** MW4-PXY CPU CARD**New Assembly:****DATE:** 16/11/00**Current Assy Rev:** Q**Current Schematic Rev:** 5.10**New Assy Rev:** R**New Schematic Rev:** 5.11

☐ CMS BOMs Updated ☒ Excel BOMs Updated ☒ Subcontractor Records Updated **Compatibility Maintained:** Yes

**IMPLEMENTATION INFORMATION:**

- |   |  |                                       |                                      |
|---|--|---------------------------------------|--------------------------------------|
| <input type="radio"/> Safety Issue        | <input type="radio"/> UL Compliance Issue  | <input type="radio"/> New Feature     | <input type="radio"/> Cost Reduction |
| <input checked="" type="radio"/> Bug Fix  | <input type="radio"/> EMC Compliance Issue | <input type="radio"/> Quality Issue   |                                      |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change      | <input type="radio"/> Cosmetic Change |                                      |

**REASON FOR CHANGE:**

Symptom: Any unreliable JTAG behaviour, including errors associated with QDCs and backplanes.

Cause: The following signals lacking integrity: TCK\_IO, TCK\_PXPY

**DETAILS OF CHANGE:**

1. Replace R301 (33R) with 10R 0603 resistor (Fairlight P/N RMD1071)
2. Replace R303 (33R) with 10R 0603 resistor (Fairlight P/N RMD1071)

NOTE: R301 and R303 are both 0603 packages. If placing 0805 parts, be \_very\_ careful to mount properly, and then check for continuity with a DVM.

5. Place revision resistors (RMD8573 - RES 10K 0.125W 1% SMD0603) as follows;  
R127 - Omit  
R128 - 10k  
R133 - Omit  
R134 - 10k  
R135 - 10k  
R136 - Omit  
R137 - Omit  
R138 - 10k

Place a assembly revision label on the card, marked "R", after the blank Rev 5 PCB P/N so it reads as " BMW4011E-R".

This ECN applies to all the boards in stock and in working process.

HW or Project Engineer.....

R&D or Project Manager.....